

15971 U.S. PTO
10/014584



12/14/01

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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

| | | | | | |
|----------------------|---------------------------|---------------------|-----------------|-------------|---------------------|
| APPL NUM 10014584 | FILING DATE 12/14/2001 | CLASS 257 438 | SUBCLASS 314 | GAU 2812 | EXAMINER Mandala |
|----------------------|---------------------------|---------------------|-----------------|-------------|---------------------|

**APPLICANTS: Furuhata Tomoyuki;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2000-382396 12/15/2000

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RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

15.57/6348

TITLE : Semiconductor devices having a non-volatile memory transistor and methods for manufacturing the same

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

| | | | |
|---|-----------|----------------------|----------------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G. |
| Assistant Examiner | | DRAWING | |
| ISSUE FEE | | Sheets Drwg. | Figs. Drwg. |
| Amount Due | Date Paid | Print Fig. | |
| Primary Examiner | | Application Examiner | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | PREPARED FOR ISSUE | |
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